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analog circuit simulations are nothing short of a masterpiece of art, the best design teams in the world have excelled in creating worlds of analog devices and processes. the cadence design system gives designers the chance to simulate design ideas with such accuracy and precision, so that digital and analog designers can work together to create the world's most complex analog circuits. manufacturing the circuitry of today and tomorrow, is the core mission of analog-ic designs. the cadence design system takes advantage of key analog design innovations to make physical verification faster and easier for the analog and digital designer. the cadence design system freely integrates with the virtuoso platform and offers a set of high-end simulation and design solutions that enable designers to complete complex analog and digital electronic designs using state-of-the-art technology. the cadence design system consists of 3 products: the cadence design system offers a set of high-end simulation and design solutions that enable to makers to complete complex analog and digital electronic designs using state-of-the-art technology. the cadence design system helps designers develop and verify complex analog and digital designs including analog and digital simulation, analog and digital physical verification, and analog and digital implementation. a delta-sigma (also known as.delta.-sigma or.sigma.) adc or dac is a high-resolution linear data conversion apparatus that is commonly used in consumer audio or video recording applications, professional audio and video equipment, and embedded systems. these systems are used to convert analog audio or video signals to digital representations of the analog signal, but with less or no noise, and with superior spectral qualities. a delta-sigma adc or dac is a device that performs analog-to-digital (a/d or d/a) and digital-to-analog (d/a or a/d) conversion

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the cadence virtuoso platform includes the product hierarchy, the integrated set of simulation acceleration technologies. as well as 3d modeling, placement, and routing. cadence virtuoso enables the creation of manufacturing robust (bom-based) designs. among other features, the cadence ic design virtuoso simplifies pcb design and implementation, providing you with the tools to quickly meet design goals and focus on the innovation that makes you a leader. the cadence virtuoso product family makes it easy to integrate electromagnetic, thermal, mechanical, and power analyses and impacts. users can choose from a variety of simulation acceleration technologies available directly in the product hierarchy. orcad and tina are more than just ic designs. they let you create robust physical models, put them in complex, realistic environments, simulate performance, and put them into the product hierarchy. for the first time, they let you optimize them together, make changes in the verification environment, and drive physical verification with data transfers and software-driven fixtures. you dont need to be an electrical engineer, ic designer, or computer scientist to get great results using the cadence virtuoso platform. design engineers and even pcb assembly technicians can work with the cadence virtuoso platform using a variety of input, display, and analysis interfaces. tina is not a general-purpose ic simulator. in fact, it is not a full-function simulator at all. its purpose is to take ic components and show them on a breadboard. with a few command-line options, you can do ic-specific things like simulate a one-transistor cell. but unlike a general-purpose ic simulator, you do not have access to all nodes in the ic design hierarchy. 5ec8ef588b

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